

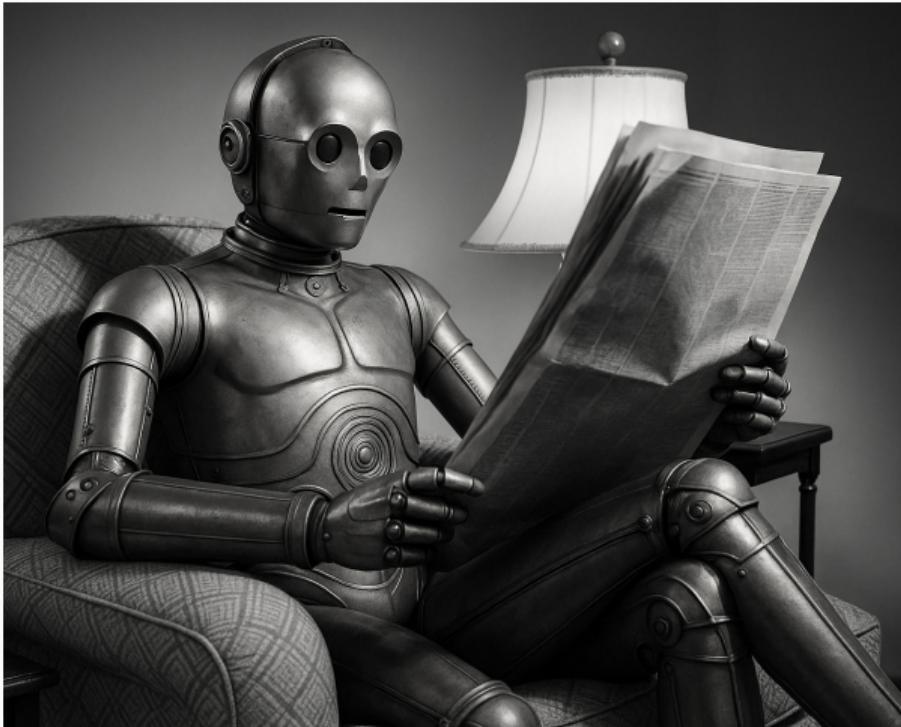
Imagine, starting your day, reading a newspaper...



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Source: ChatGPT

Crowdstrike 2024 outage

- Crowdstrike: airport IT security software, used globally
- 19 July 2024: Crowdstrike software update
- Problem: at start-up, software received 20 instead of 21 pieces of data
- *Expensive logic bug: possibly 5 billion dollars lost¹*



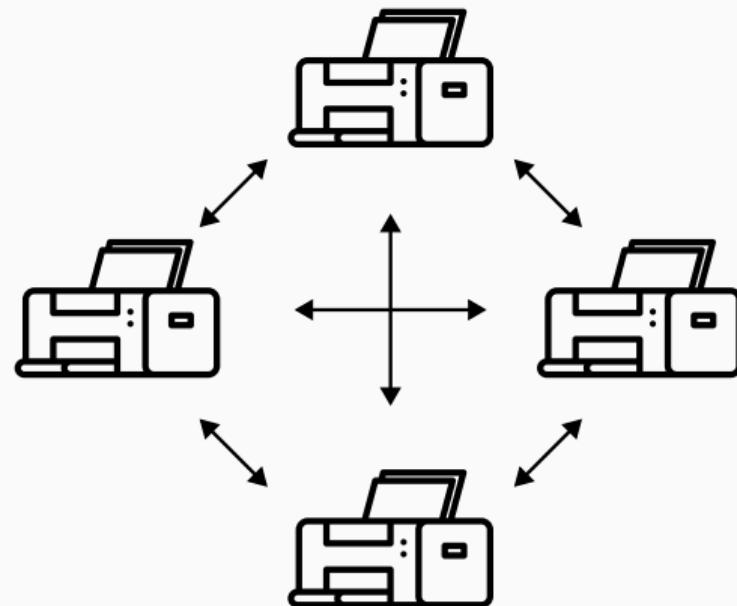
Source: Smishra1, Wikimedia Commons

¹<https://edition.cnn.com/2024/07/24/tech/crowdstrike-outage-cost-cause>

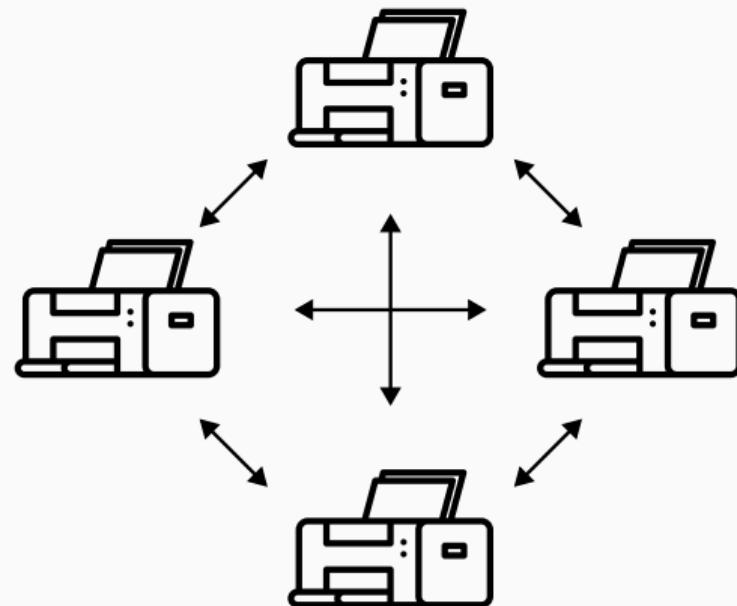
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- Trend: demands for faster software are growing
- Solution: more things at the same time



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- Logic bugs are not the only problem
- Trend: demands for faster software are growing
- Solution: more things at the same time
- Concurrency bugs are hard



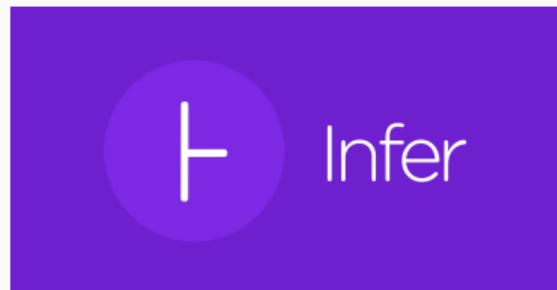
- One possible solution: formal methods
- Mathematical techniques to prevent faults
- Insight: saying *what* you want is easier than saying *how*
- Can prevent logic *and* concurrency bugs

$$\frac{\Gamma \vdash \Delta, A \quad A, \Sigma \vdash \Pi}{\Gamma, \Sigma \vdash \Delta, \Pi}$$

$$\{ P \} \subset \{ Q \}$$

Formal methods and industry

- Formal methods are promising
- But: limited uptake
- Some successes: TLA⁺ @ AWS, Pulse/Infer @ FB, ...
- No standard tool yet
- Barrier to adoption still too high
- Difficult to express mental models in formal notation



How to narrow the gap between mental models and formal methods?

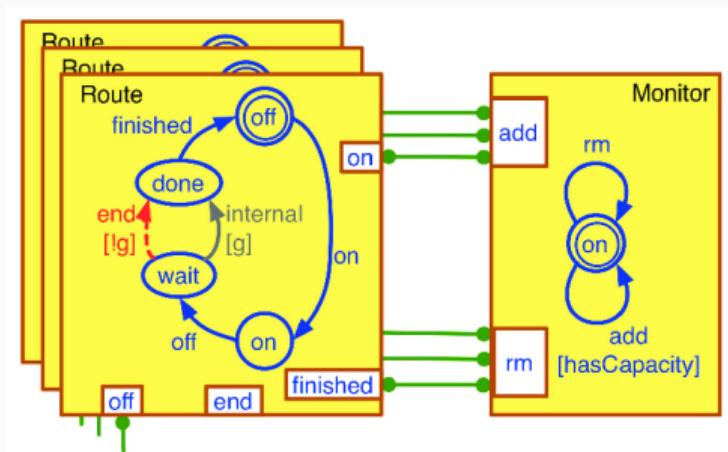
- Starting point: program verifier VerCors
- Industry experience
- Formal methods and software development
- Formal methods and distributed systems



- Applied VerCors at Technolusion to tunnel control software
- Found two problems: relevance
- But, also: difficult to apply, difficult to explain
- Goals:
 - Bring VerCors closer to developer mental models
 - Improve language support

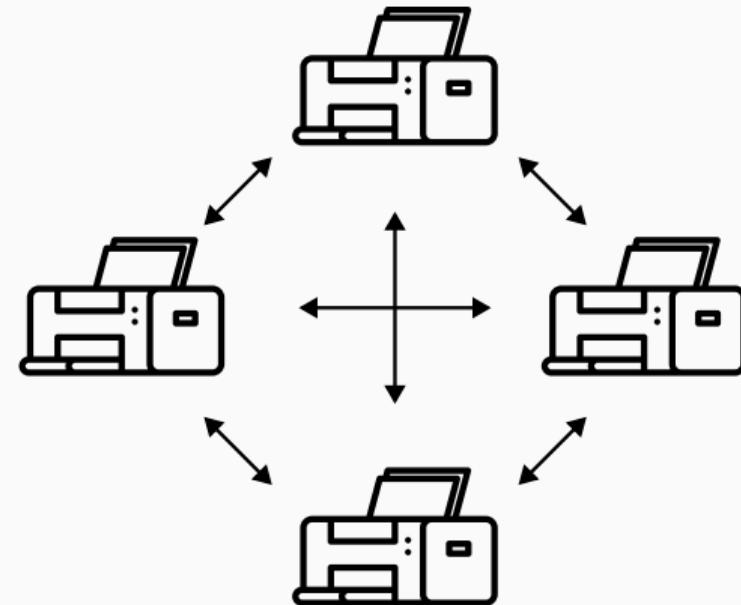


- Unify formal methods and software development
- Novel combination:
 - JavaBIP: component-based software development
 - VerCors: concurrent program verifier
- Showed feasibility, effectiveness, reuse

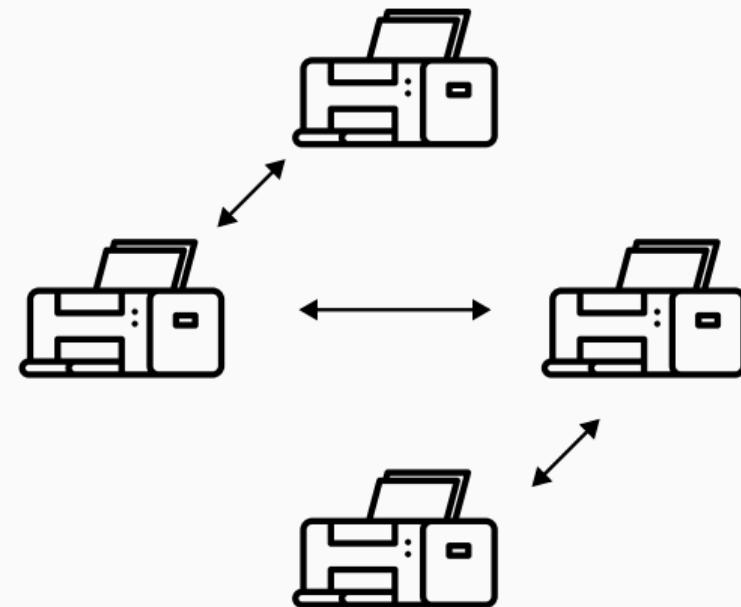


Source: Anastasia Mavridou

- Choreographies are a DSL for designing distributed systems
- We extended existing verification tool VeyMont:
 - Shared memory
 - Parameterization
- Case studies done with VeyMont can now be more realistic



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Conclusion

- Formal methods are promising
- But: uptake is limited
- Improved insights into the needs of the industry
- Created and improved two tools to better cater to industry needs
- Also, showing their effectiveness

